

IN THE SPECIFICATION

Please amend the fourth paragraph on page 14, lines 19-24 as shown below.

--The final multiplexer 99 receives the carry input  $C_i$  and its inverse. A fourth inverter 110 is provided to provide the inverse of the carry input  $[\overline{C_i}]$ . The select signal is provided by the output of the fourth multiplexer 96. The output of the sixth multiplexer is input to a third buffer 112, the output of which provides the sum output  $S$ .--